

Notice of References Cited	Application/Control No. 10/016,693	Applicant(s)/Patent Under Reexamination LAMMERT, MICHAEL D.	
	Examiner Julio J. Maldonado	Art Unit 2823	Page 1 of 1

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*	B	US-6,410,414 B1	06-2002	Lee, Joo-hern	438/612
*	C	US-6,387,783 B1	05-2002	Furukawa et al.	438/574
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NON-PATENT DOCUMENTS

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	U	Wolf, Stanley, Silicon Processing for the VLSI Era, Volume 2: Process Integration; 1990 by Lattice Press, pages 214-215
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*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.